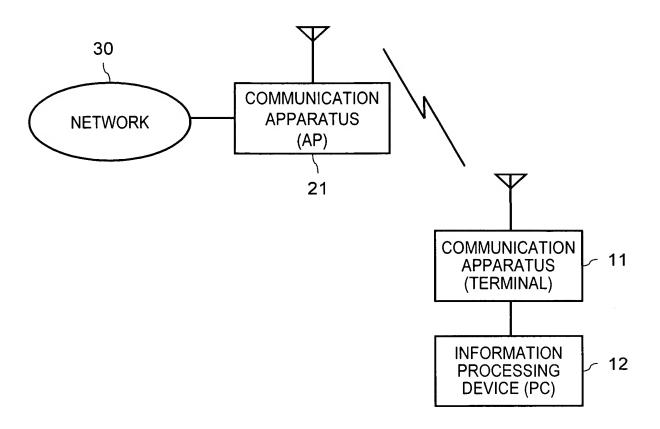
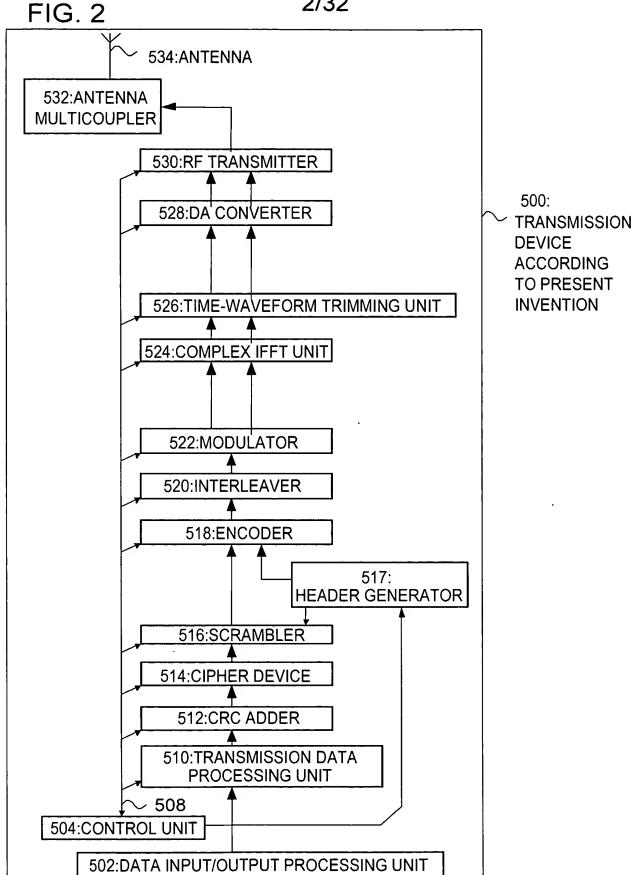
FIG. 1

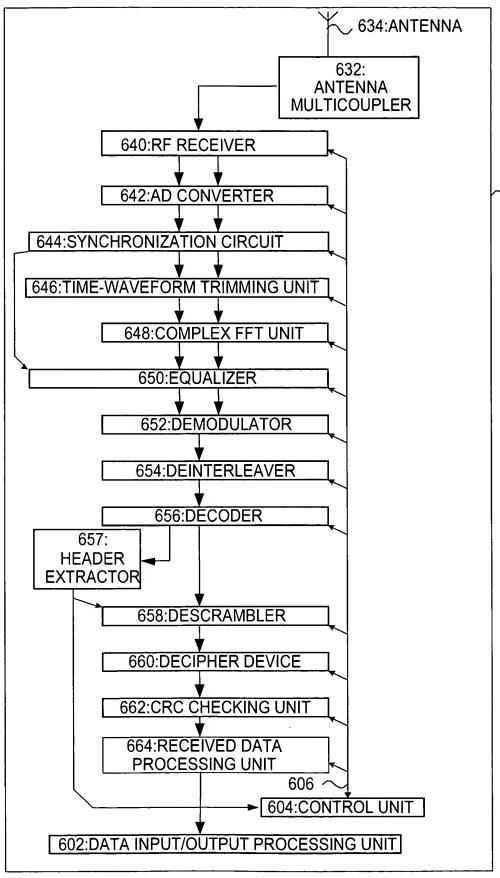


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600: RECEPTION DEVICE ACCORDING TO PRESENT INVENTION

FIG. 4

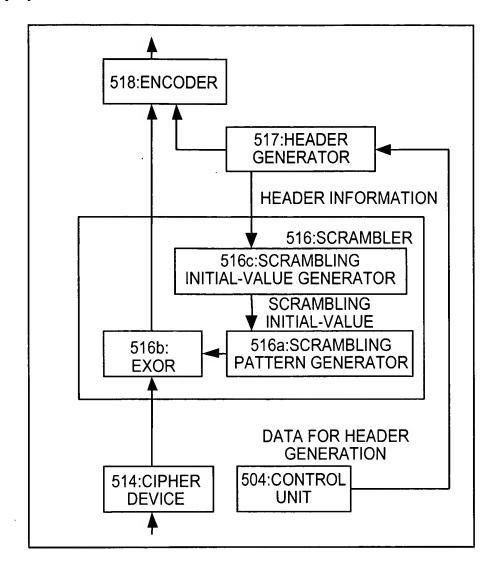


FIG. 5

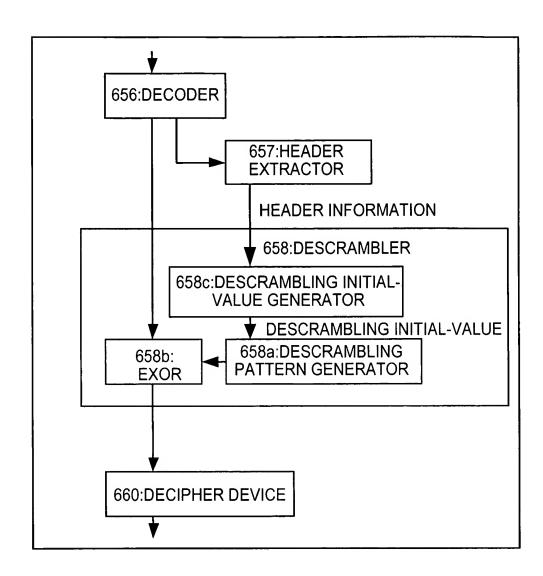


FIG. 6

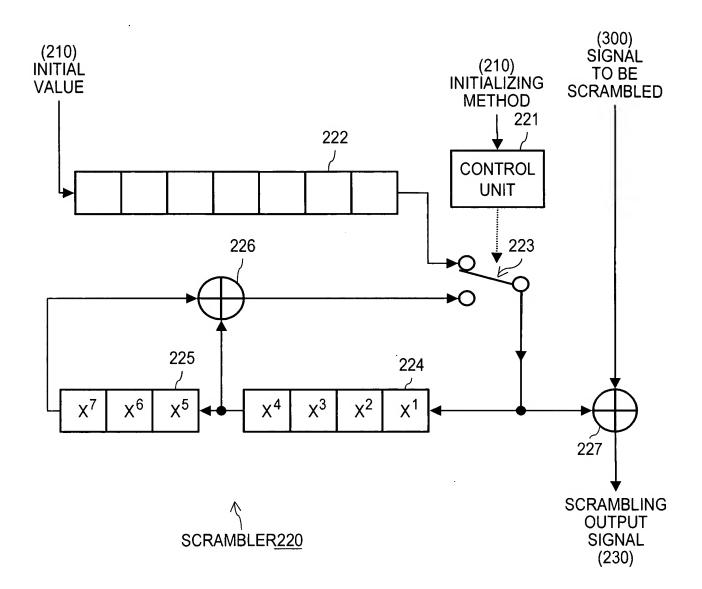
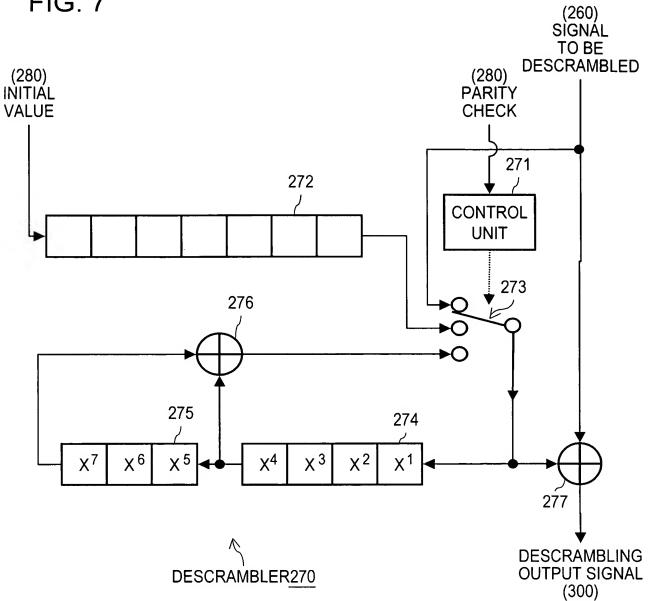


FIG. 7



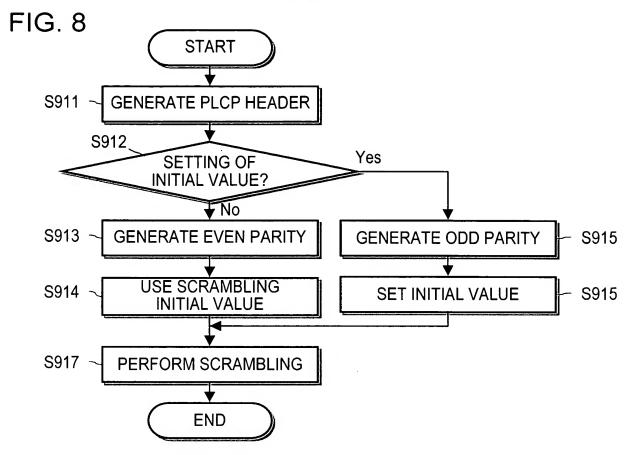


FIG. 9

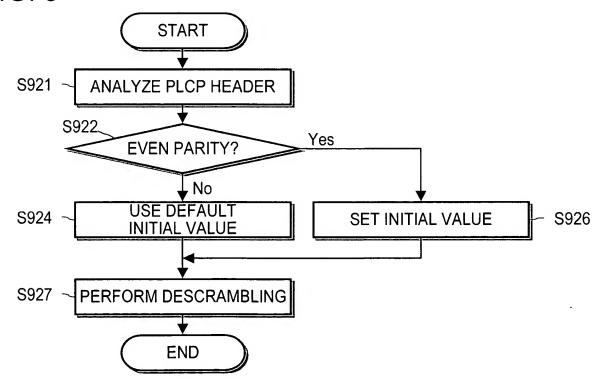
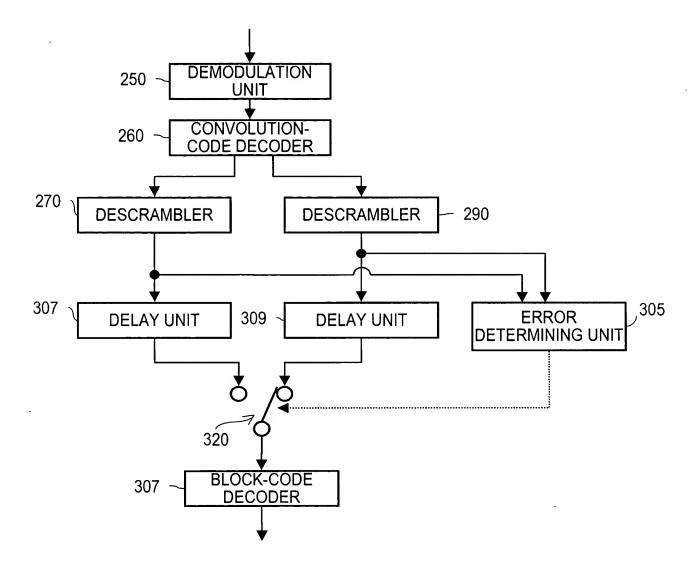


FIG. 10



(260)



FIG. 11

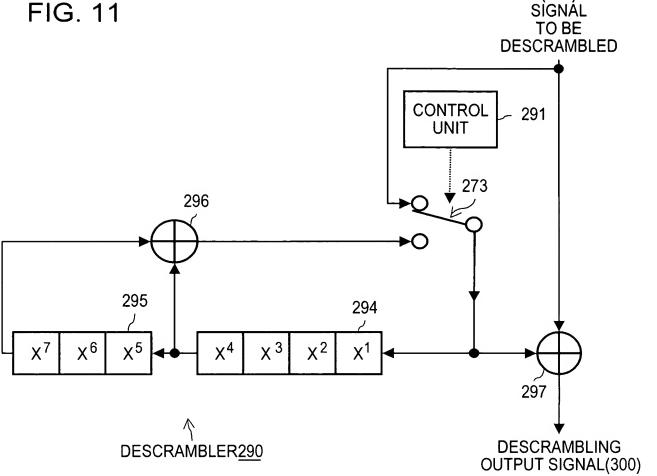


FIG. 12

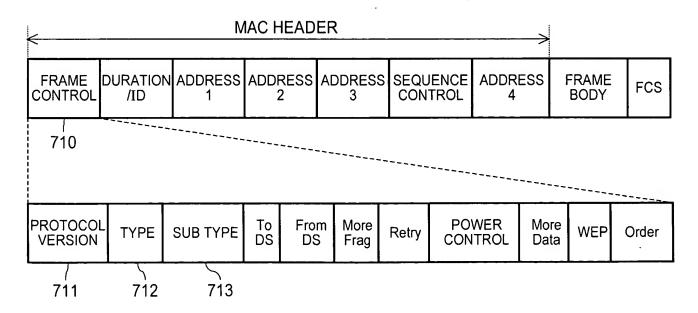
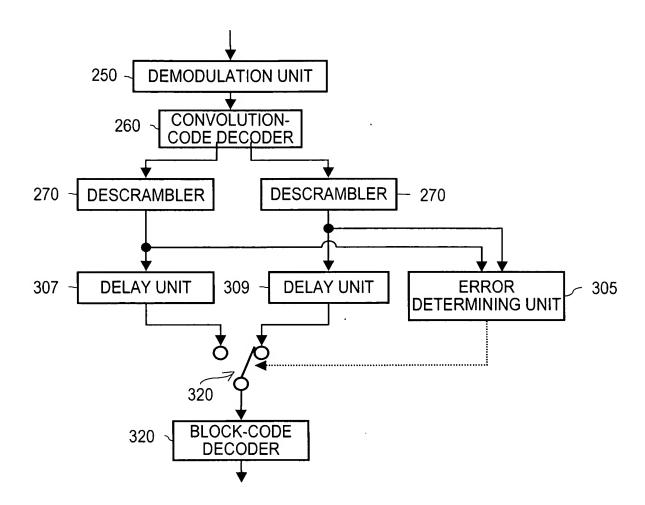


FIG. 13



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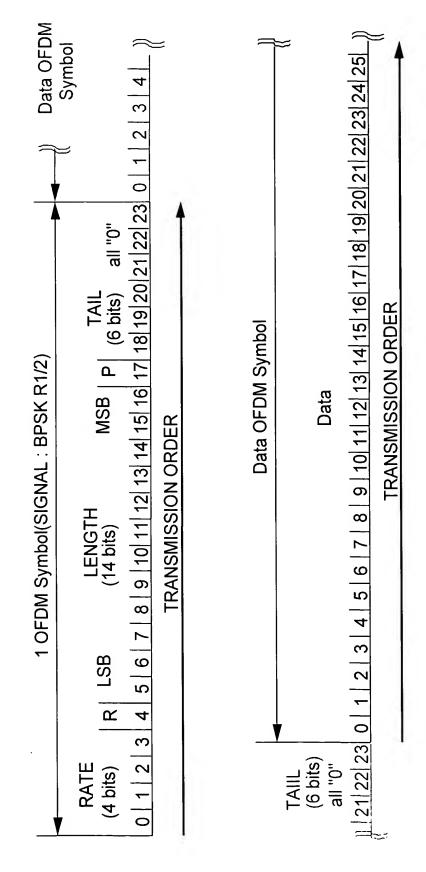
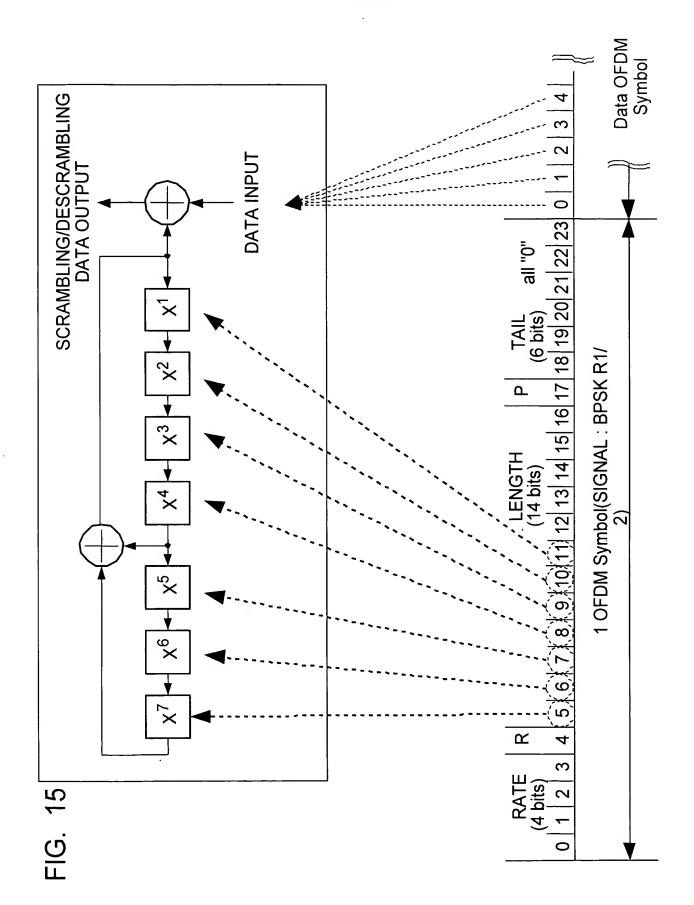


FIG. 14



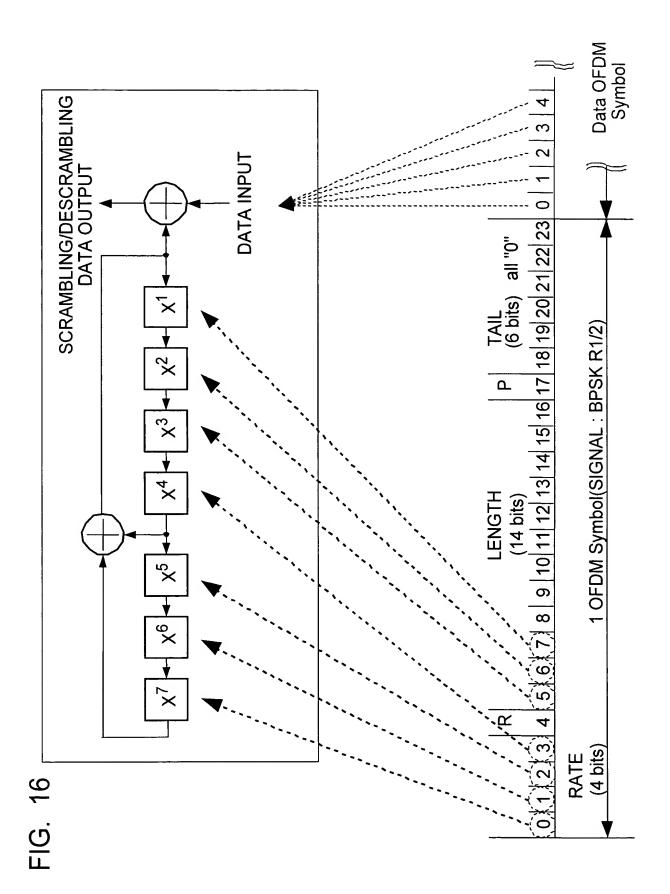
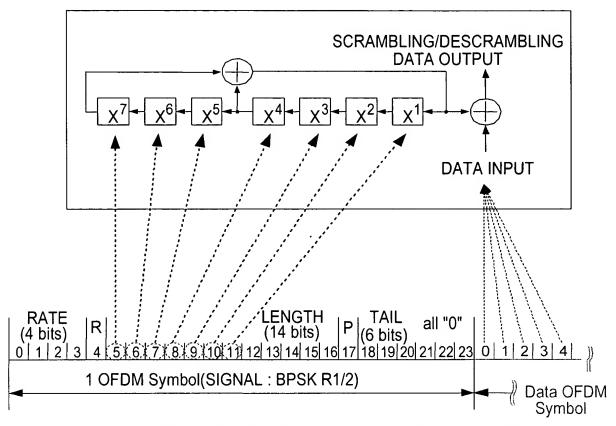
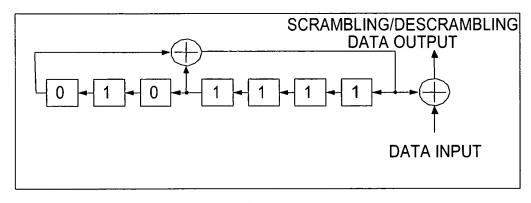


FIG. 17



(a) CASE THAT EXTRACTED BITS ARE NOT ALL ZERO



(b) CASE THAT EXTRACTED BITS ARE ALL ZERO

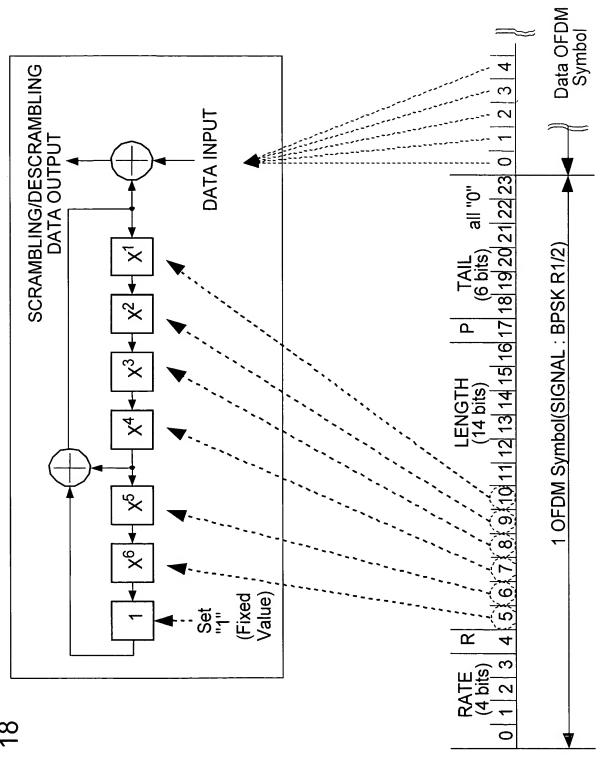


FIG. 1

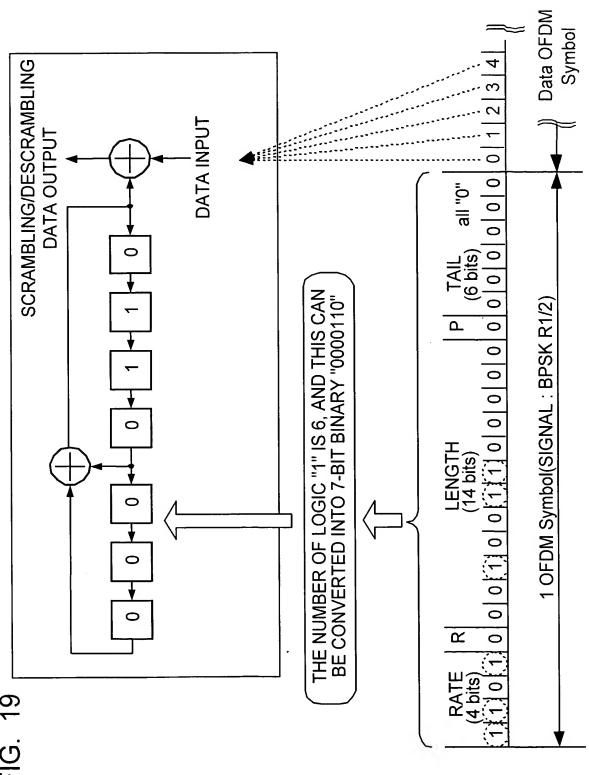
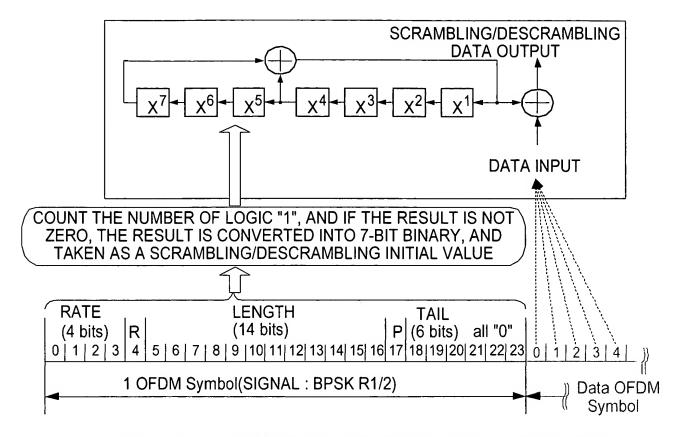
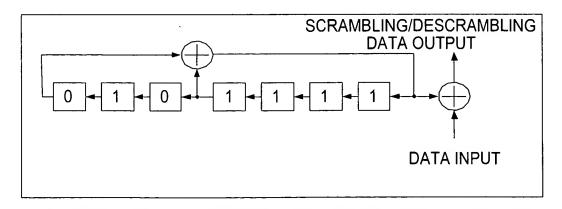


FIG. 20



(a) CASE THAT THE NUMBER OF BITS HAVING LOGIC "1" IS NOT ZERO



(b) CASE THAT THE NUMBER OF BITS HAVING LOGIC "1" IS ZERO

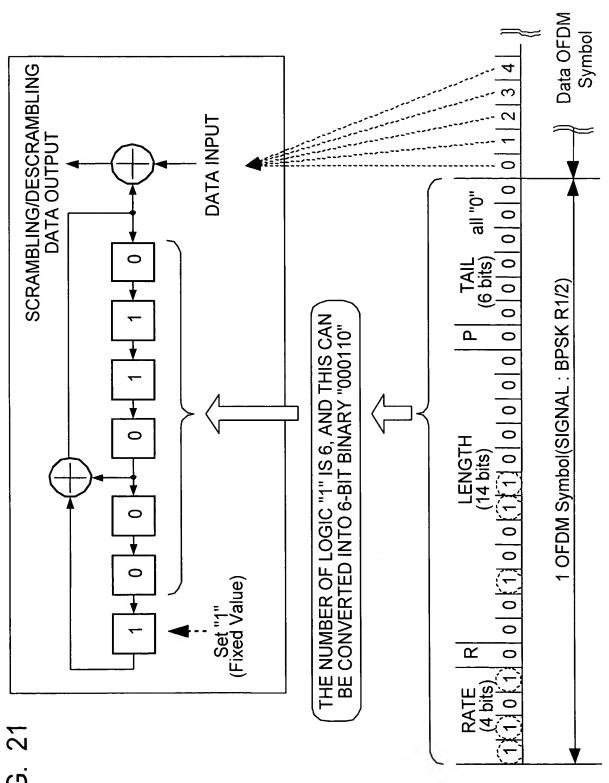


FIG. 21

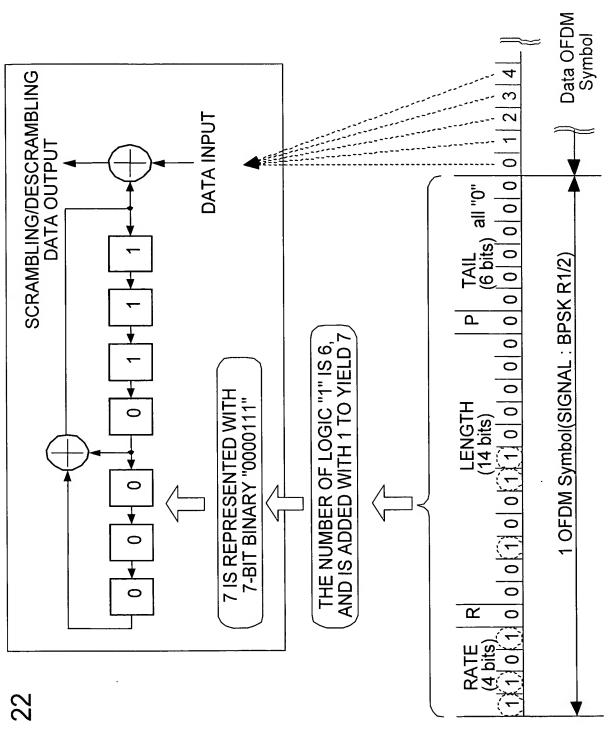


FIG. 22

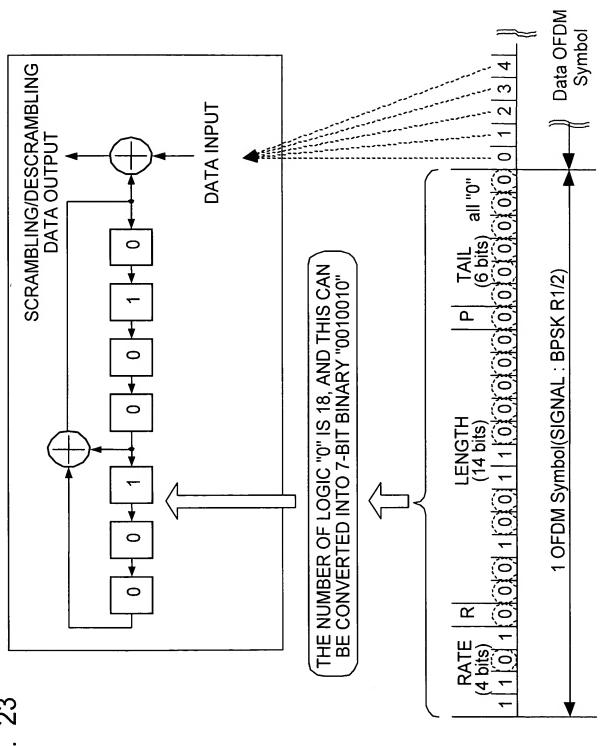
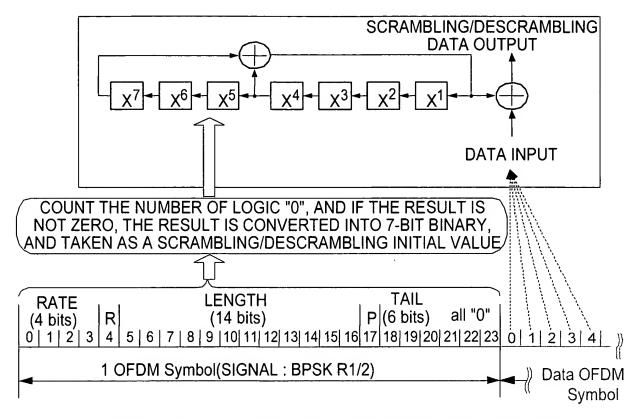
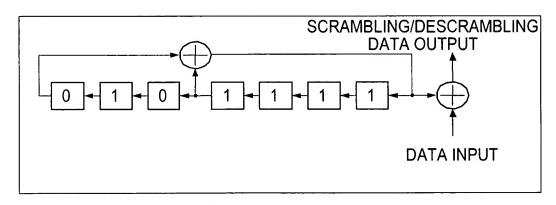


FIG.

FIG. 24



(a) CASE THAT THE NUMBER OF BITS HAVING LOGIC "0" IS NOT ZERO



(b) CASE THAT THE NUMBER OF BITS HAVING LOGIC "0" IS ZERO

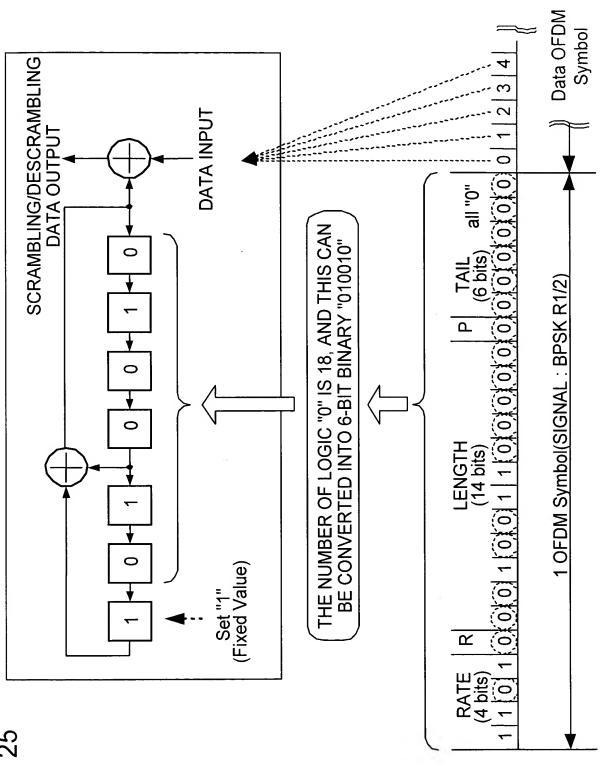


FIG. 2

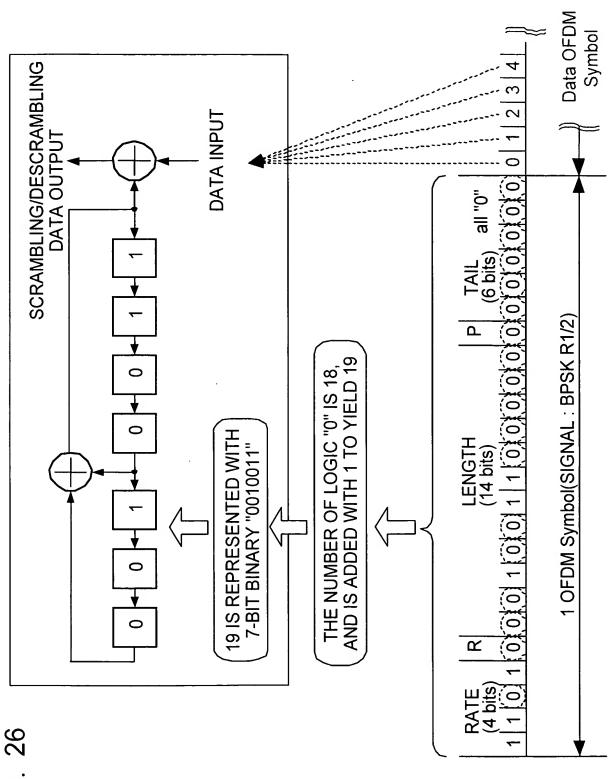


FIG. 26

FIG. 27

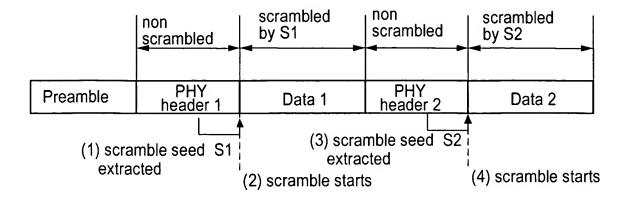


FIG. 28

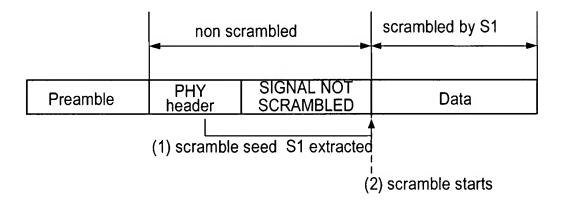
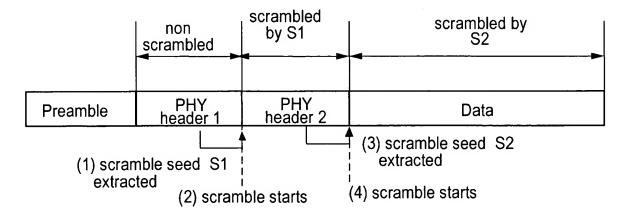
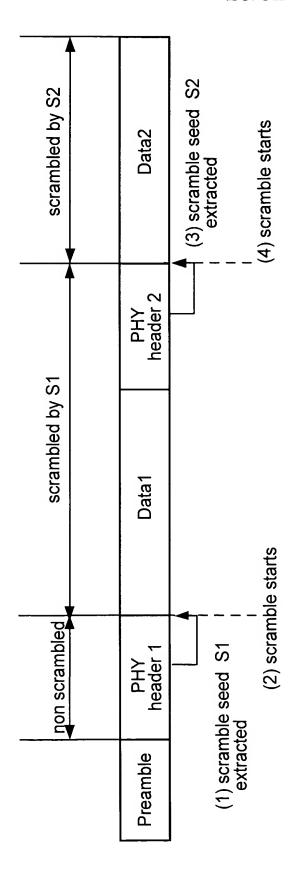
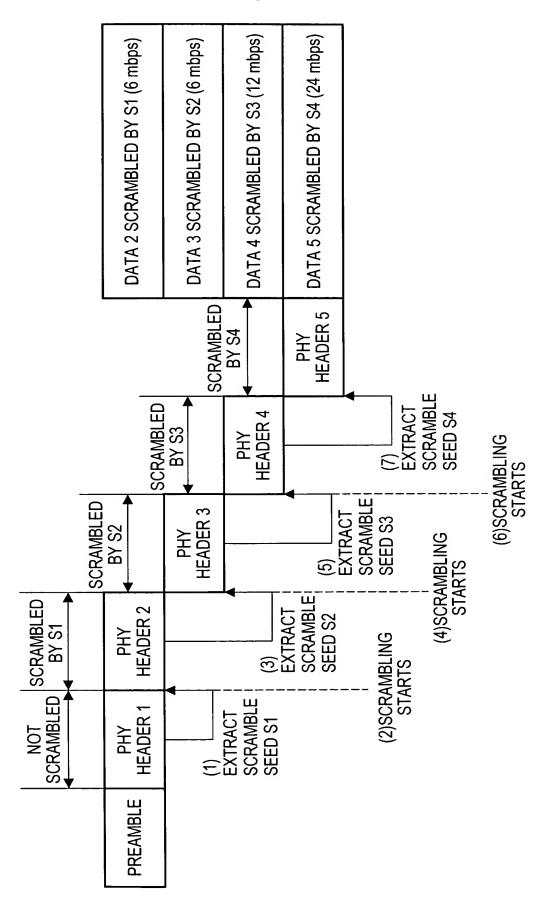


FIG. 29





-1G.



=[G. 3

FIG. 32

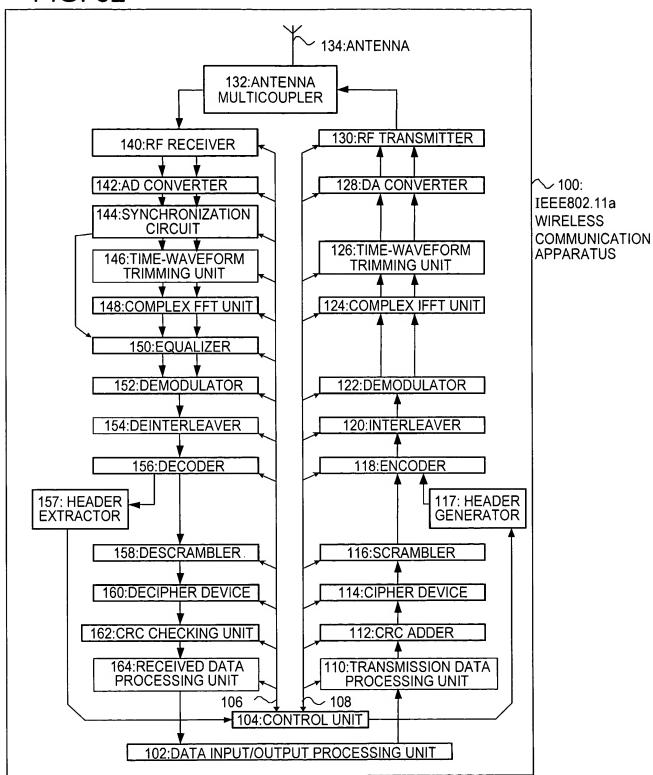


FIG. 33

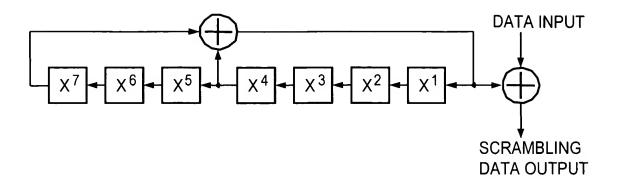
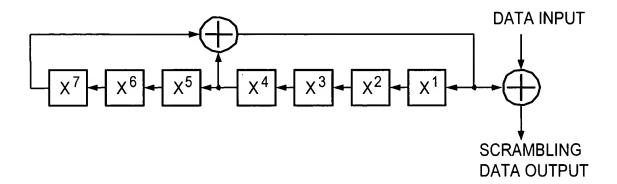
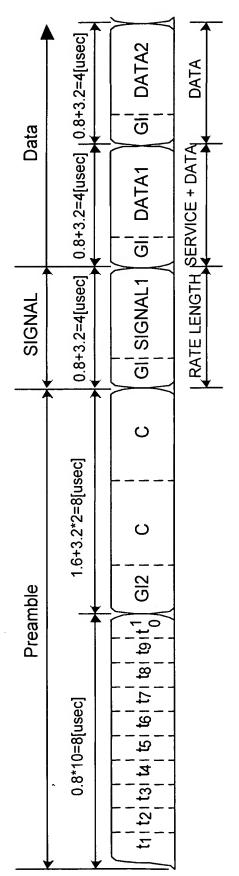


FIG. 34



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:IG: 35

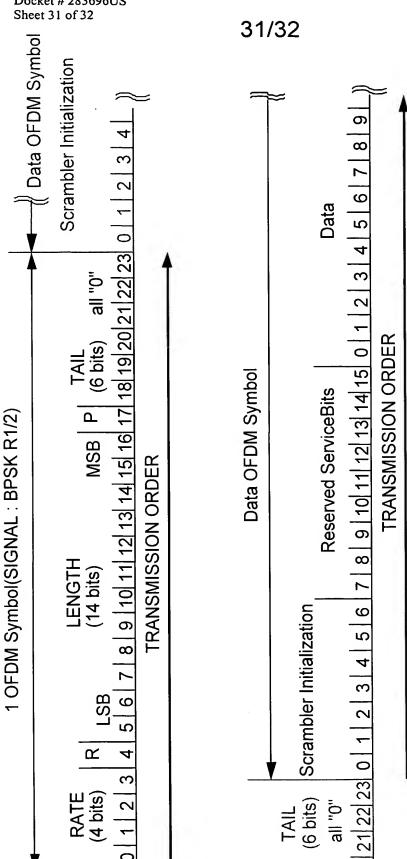


FIG. 36



FIG. 37

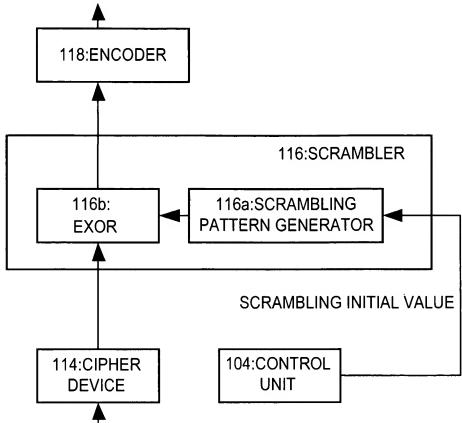


FIG. 38

